



**REPLY UNDER 37 C.F.R. § 1.116  
EXPEDITED PROCEDURE  
EXAMINING GROUP 2188**

**IN THE UNITED STATES PATENT AND TRADEMARK OFFICE**

Application No.: 10/817,632

Filed: April 2, 2004

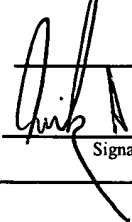
Inventor(s):  
Landin, et al.

Title: MULTI-NODE SYSTEM  
IN WHICH HOME  
MEMORY SUBSYSTEM  
STORES GLOBAL TO  
LOCAL ADDRESS  
TRANSLATION  
INFORMATION FOR  
REPLICATING NODES

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Examiner: Doan, Duc T.  
Group/Art Unit: 2188  
Atty. Dkt. No: 5181-95501

I hereby certify that this correspondence is being deposited with the United States Postal Service with sufficient postage as first class mail in an envelope addressed to Commissioner for Patents, P.O. Box 1450, Alexandria, VA 22313-1450, on the date indicated below.

  
Erik A. Heter  
Printed Name  
January 22, 2007  
Date  
Signature

**RESPONSE TO OFFICE ACTION OF  
October 20, 2006**

**ATTN: Box A.F.**  
Commissioner for Patents  
P.O. Box 1450  
Alexandria, VA 22313-1450

Dear Sir:

This paper is submitted in response to the Office Action of October 20, 2006, to further highlight why the application is in condition for allowance.

Please amend the case as listed below.